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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Akram et al.**Patent No.:** 6,730,526 B2**Issued:** May 4, 2004**For:** MULTI-CHIP MODULE SYSTEM
AND METHOD OF FABRICATION**Attorney Docket No.:** 2269-2754.5US

CERTIFICATE OF MAILING

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January 11, 2007

Date

Signature

Leta M. Howard

Name (Type/Print)

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OFFICE MISTAKES (37 C.F.R. § 1.322)**

Attn.: Certificate of Corrections Branch
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Sir:

It is noted that several errors appear in this patent of a typographical nature. These errors are due to mistakes in printing on the part of the U.S. Patent and Trademark Office, and occurred through no fault of the Applicants. A certificate of correction in the form attached hereto is requested.

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Attached hereto in duplicate is Form PTO/SB/44 with at least one copy being suitable for printing.

Respectfully submitted,



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Date: January 11, 2007
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Enclosures: PTO/SB/44 in duplicate

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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO : 6,730,526 B2
DATED : May 4, 2004
INVENTOR(S) : Salman Akram, David R. Hembree, and James M. Wark

Page 1 of 1

It is certified that errors appear in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

On the title page:

In ITEM (56), "References Cited,
U. S. Patent Documents,"
PAGE 1, COLUMN 2,

Add the following Documents,

5,448,165	09/1995	Hodge et al.
5,461,544	10/1995	Ewers
5,468,655,	11/1995	Greer
5,475,317	12/1995	Smith
5,517,515	05/1996	Spall et al.
5,703,760	12/1997	Zhu
5,723,907	03/1998	Akram
5,754,410	05/1998	Bardsley et al.
5,807,762	09/1998	Akram et al.
5,956,233	09/1999	Yew et al.
5,959,310	09/1999	Akram et al.

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James R. Duzan
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